



DOCKET NO: 198786US-2 RE

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

MITSUHIRO YANO, ET AL.
SERIAL NO: 09/891,925
FILED: JUNE 27, 2001
FOR: SEMICONDUCTOR DEVICE AND
METHOD OF FABRICATING SAME

: EXAMINER: CRANE, S.

:

: GROUP ART UNIT: 2811

:

SUPPLEMENTAL REISSUE DECLARATION UNDER 37 C.F.R. § 1.132

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

We, Mitsuhiro Yano and Kouichi Mochizuki, declare that, supplemental to the Reissue Declaration filed June 27, 2001:

1. We further verily believe ourselves to be the original and first inventors of the invention described and claimed in U.S. letters patent no. 5,945,692 for a "SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING SAME", granted August 31, 1999,

2. We declare that all statements made herein are of our own knowledge and are true and that all statements made of information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of this application or any patent issuing thereon.

Mitsuhiro Yano

Mitsuhiro Yano

M. Y, May 29, 2007

Mitsuhiro Yano

May 29, 2007

Date

Kouichi Mochizuki

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Date

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